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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/970,297	10/02/2001	Sean S. Chen	NSC-P05052	9656
759	07/13/2004		EXAM	INER
WAGNER, MURABITO & HAO LLP			CUNNINGHAM, TERRY D	
Third Floor Two North Market Street			ART UNIT	PAPER NUMBER
San Jose, CA 95113			2816	

DATE MAILED: 07/13/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

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		Application No.	Applicant(s)			
Office Action Summary		09/970,297	CHEN, SEAN S.			
		Examiner	Art Unit			
		Terry D. Cunningham	2816			
Period fo	The MAILING DATE of this communication a or Reply	appears on the cover sheet with ti	ne correspondence address			
THE - Exte after - If the - If NC - Failt Any	IORTENED STATUTORY PERIOD FOR REI MAILING DATE OF THIS COMMUNICATION INSIGNS of time may be available under the provisions of 37 CFR (SIX (6) MONTHS from the mailing date of this communication. Per period for reply specified above is less than thirty (30) days, a period for reply specified above, the maximum statutory perior to reply within the set or extended period for reply will, by stareply received by the Office later than three months after the material patent term adjustment. See 37 CFR 1.704(b).	N. 1.136(a). In no event, however, may a reply by the reply within the statutory minimum of thirty (30 iod will apply and will expire SIX (6) MONTHS tute, cause the application to become ABAND	e timely filed  days will be considered timely. from the mailing date of this communication.  DNED (35 U.S.C. § 133).			
Status						
1)⊠	Responsive to communication(s) filed on 24	1 May 2004.				
· · —						
3)□	<del>_</del>					
	closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.					
Disposit	ion of Claims					
5)□	Claim(s) 1-4,6-8,11-13,15-21 and 23 is/are 4a) Of the above claim(s) is/are without claim(s) is/are allowed.  Claim(s) 1-4,6-8,11-13,15-21 and 23 is/are  Claim(s) is/are objected to.  Claim(s) are subject to restriction and	Irawn from consideration.				
Applicat	ion Papers					
10)⊠	The specification is objected to by the Exame The drawing(s) filed on 20 October 2001 is/a Applicant may not request that any objection to the Replacement drawing sheet(s) including the correct The oath or declaration is objected to by the	are: a)⊠ accepted or b)□ object the drawing(s) be held in abeyance. rection is required if the drawing(s) is	See 37 CFR 1.85(a). objected to. See 37 CFR 1.121(d).			
Priority (	under 35 U.S.C. § 119					
12)□ a)	Acknowledgment is made of a claim for forei  All b) Some * c) None of:  1. Certified copies of the priority docume  2. Certified copies of the priority docume  3. Copies of the certified copies of the p  application from the International Bure  See the attached detailed Office action for a l	ents have been received. ents have been received in Application of the contract of the contrac	cation No eived in this National Stage			
Attachmen	t(s)					
	e of References Cited (PTO-892)	4) Interview Summ				
3) 🔲 Infori	e of Draftsperson's Patent Drawing Review (PTO-948) mation Disclosure Statement(s) (PTO-1449 or PTO/SB/0 ir No(s)/Mail Date	Paper No(s)/Ma  5)  Notice of Inform  6)  Other:	l Date al Patent Application (PTO-152)			

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## **DETAILED ACTION**

## Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claims 1-4, 6-8, 11-13, 15-21 and 23 are rejected under 35 U.S.C. 103(a) as being unpatentable over Kadanka et al. (USPN 5,621,308) in view of newly cited art to Mietus (USPN 5,666,046). Kadanka et al. disclose, in Fig. 2, a circuit comprising: "a band-gap reference unit (73)"; "a buffer circuit (54)"; and "a voltage pull-up device (70)", wherein the "voltage pull-up device" has a "transistors (48)". Kadanka et al. does not expressly disclose that transistor 46 has a "less than  $1.0 \, V_{BE}$ ". However, it is notoriously well known, as expressly taught by Mietus (e.g., see Col. 1, lines 56-67), to use a voltage of 0.7 volts for the expect advantage of using a lower supply voltage (e.g., 0.8 volts). Therefore, it would have been obvious for one skilled in the art to manufacture transistor 48 with "less than  $1.0 \, V_{BE}$ " for the expected advantage allowing for a lower supply voltage.

Examiner has fully considered Applicant's remarks for the above rejection and has not found them to be persuasive. Applicant argues concerning element 70 stating that such is a "voltage regulator". However, this is not consistent with what would be understood by one skilled in the art and by what is stated by Kadanka et al. Kadanka et al. expressly discloses that element 70 of Fig. 2 is a "regulator portion" and more specifically a "current mirror". A current mirror is notoriously well known as being a stabilized or regulated current source, not a "voltage

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regulator". Applicant further argues that element 70 is not "a voltage pull-up device". Applicant provides the unnecessarily narrow definition of a "pull-up device" a being a device "used to pull a node up to a particular voltage when it is active while a voltage regulator functions to provide a constant voltage to components of a circuit". This definition is not seen to be consistent with what one skilled in the art would deem the broadest reasonable interpretation of "a voltage pull-up device". Firstly, it is not even understood how one skilled in the art would require this definition to include anything concerning "a voltage regulator". Further, the discussion concerning operation "when it is active" would appear to relate exclusively to a switch, which again is clearly unnecessarily limited. Examiner contends that one skilled in the art would deem the broadest reasonable interpretation for "a voltage pull-up device" as being a device that sources current, regardless of whether such has "active" or inactive states.

With respect to the language concerning the language "to reduce a required supply voltage to maintain a band-gap reference voltage", it is clear that using a transistor with a " $V_{BE}$  of less than 1.0 volts" would provide this operation. Clearly, using a transistor with a " $V_{BE}$  of less than 1.0 volts" would reduce the required supply voltage from a circuit using a transistor with a  $V_{BE}$  of 2.0 volts, for example. Thus, the above combination is seen to meet the claim language.

THIS ACTION IS MADE FINAL. Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period

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will expire on the date the advisory action is mailed, and any extension fee pursuant to 37

CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event,

however, will the statutory period for reply expire later than SIX MONTHS from the mailing

date of this final action.

Conclusion

Any inquiry concerning this communication or earlier communications from the

examiner should be directed to Terry Cunningham whose telephone number is 571-272-1742.

The examiner can normally be reached on Monday-Thursday from 7:30 to 6:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's

supervisor, Timothy P. Callahan can be reached on 571-272-1740. The fax phone number for

the organization where this application or proceeding is assigned is (703) 872-9306.

Information regarding the status of an application may be obtained from the Patent

Application Information Retrieval (PAIR) system. Status information for published applications

may be obtained from either Private PAIR or Public PAIR. Status information for unpublished

applications is available through Private PAIR only. For more information about the PAIR

system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR

system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

TC

July 12, 2004

Terry D. Cunningl

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